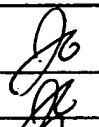
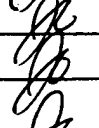
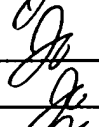
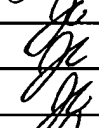
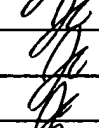
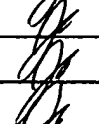
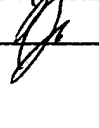


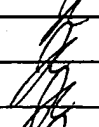
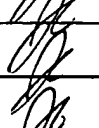


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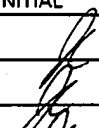
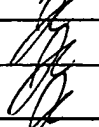
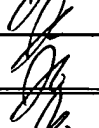
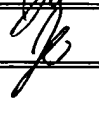


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Serial No.: Unk.
Applicant: Remmelt Pit et al.
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Examiner Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date
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	57-210479	12/24/82	Japan	G11B17/32	G11B5/60	Abstract
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	JP6267219	09/22/94	Japan	G11B21/21		Abstract
	0 242 597 A2	03/20/87	Europe	G11B21/20	G11B5/54	Yes

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<i>Jc</i>	"Ramp loading "sweet spots". Yaeger and Hiller. Microsystem Technologies, Vol. V.8, Pt No. 1, pp. 41-46, 2002.
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Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.